

FIG. 1

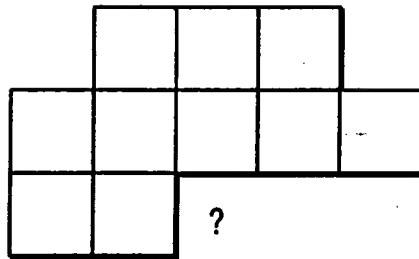


FIG. 2A

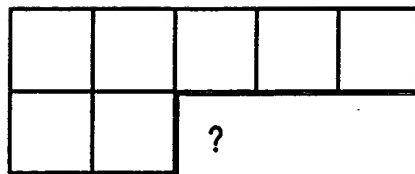


FIG. 2B

ORIGINAL IMAGE

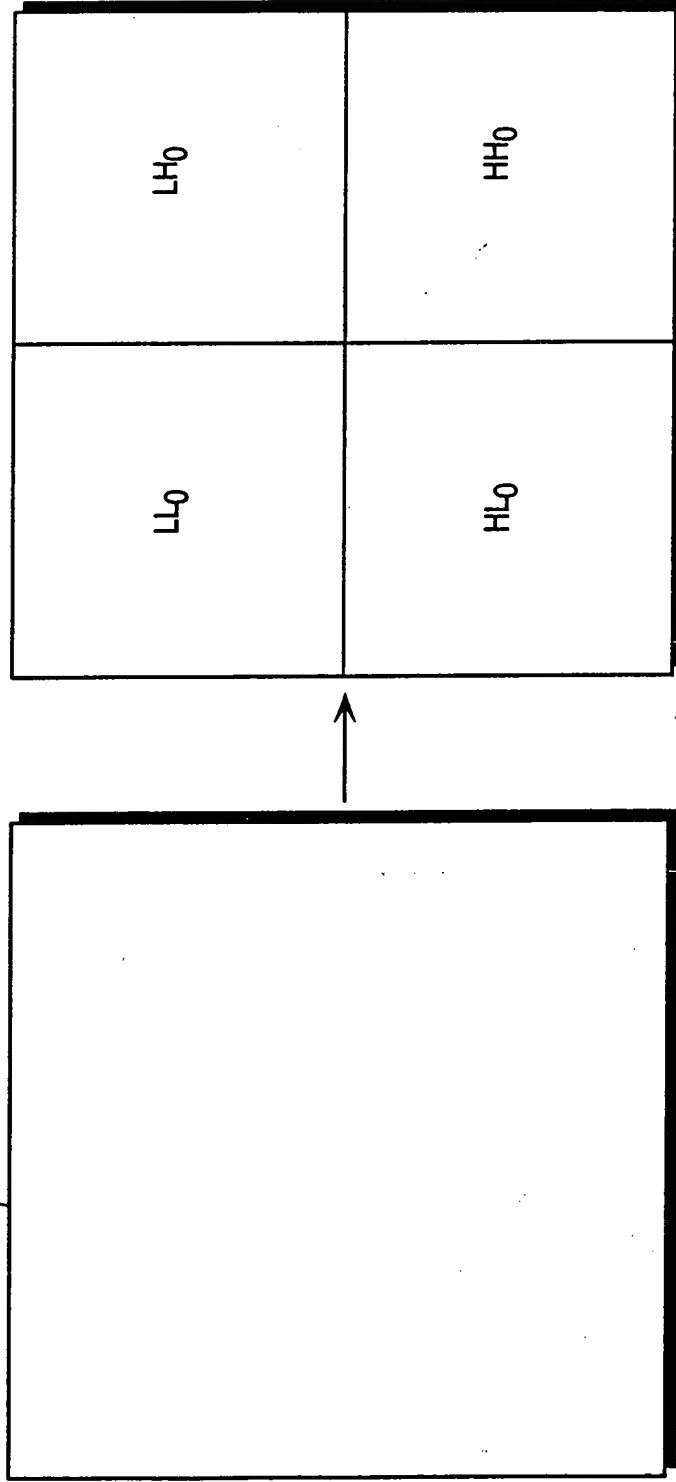


FIG. 3A

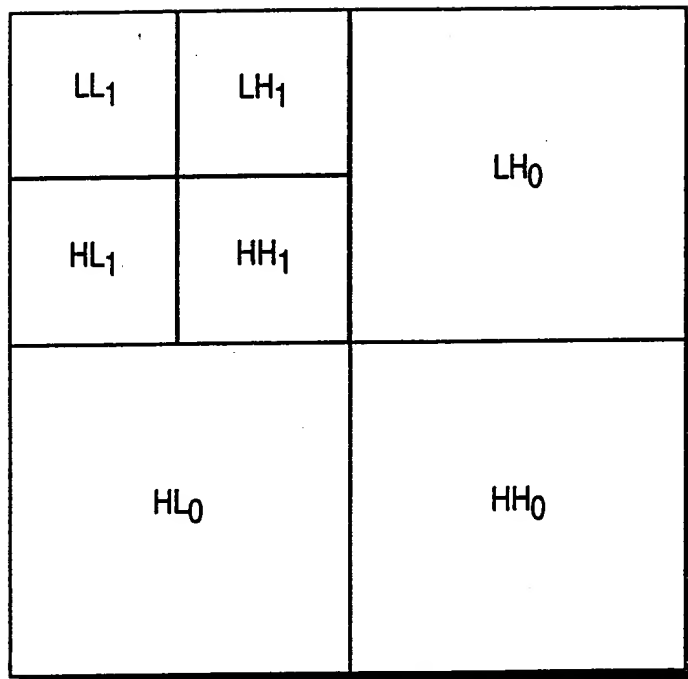


FIG. 3B

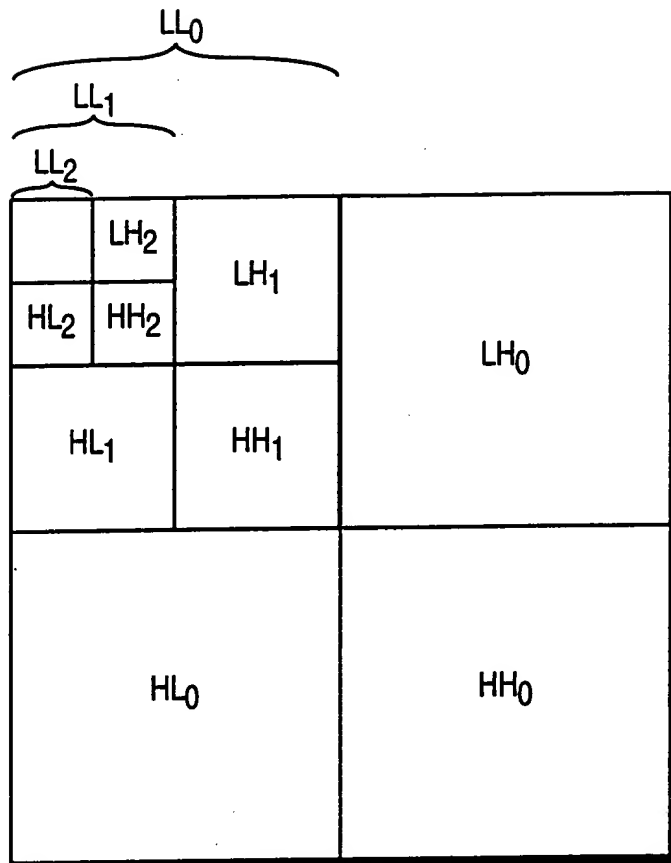


FIG. 3C

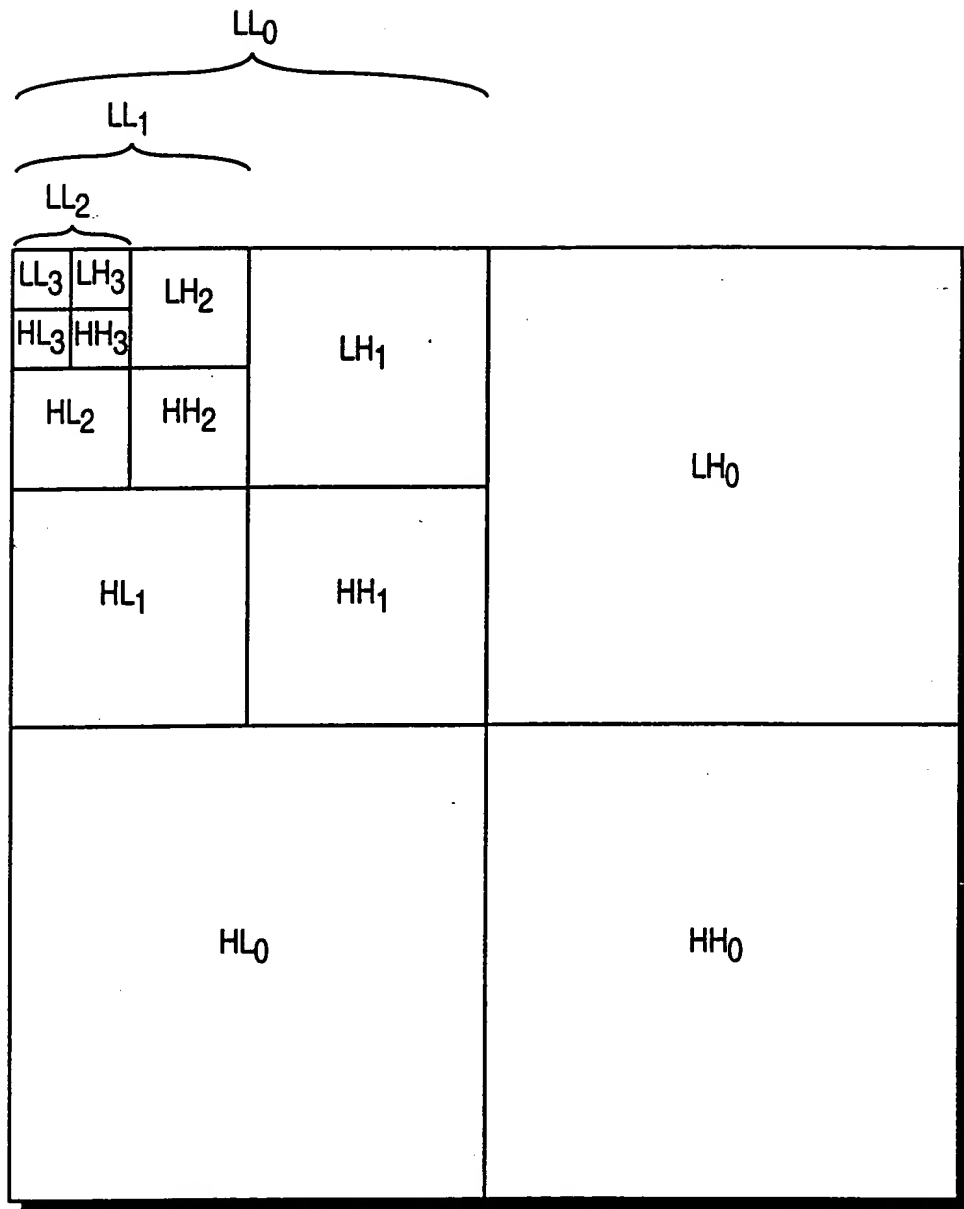


FIG. 3D

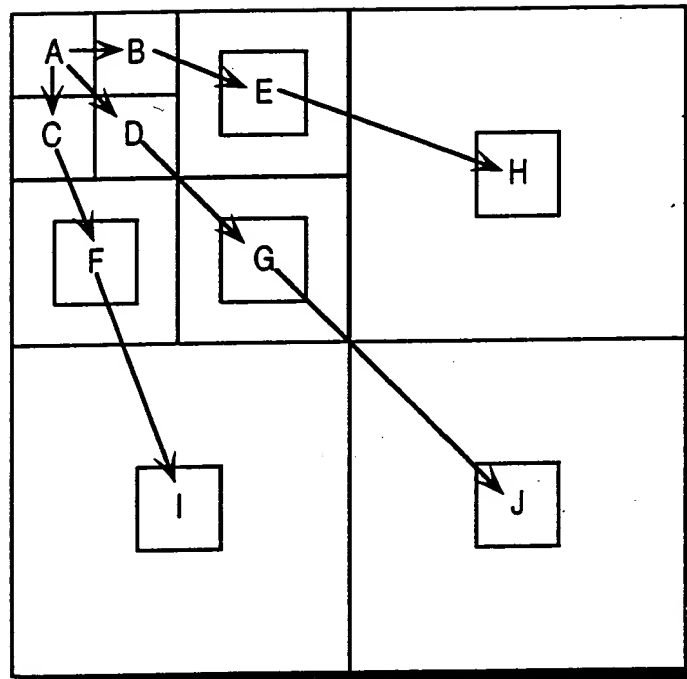
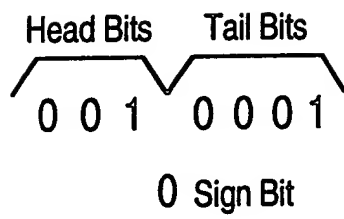


FIG. 4

0	1	2	3	4	5	
6	7	8	9	10	11	
12	13	14	15	16	17	
18	19	20	21	22	23	
24	25	26	27	28	29	
30	31	32	33	34	35	
36	37	38	39	40	41	

FIG. 6

8-bit Value = 17



8-bit Value = -8

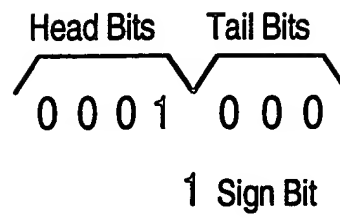


FIG. 7

b	b+2	b+2
b+2	b+4	
b+2		b+4

FIG. 8A

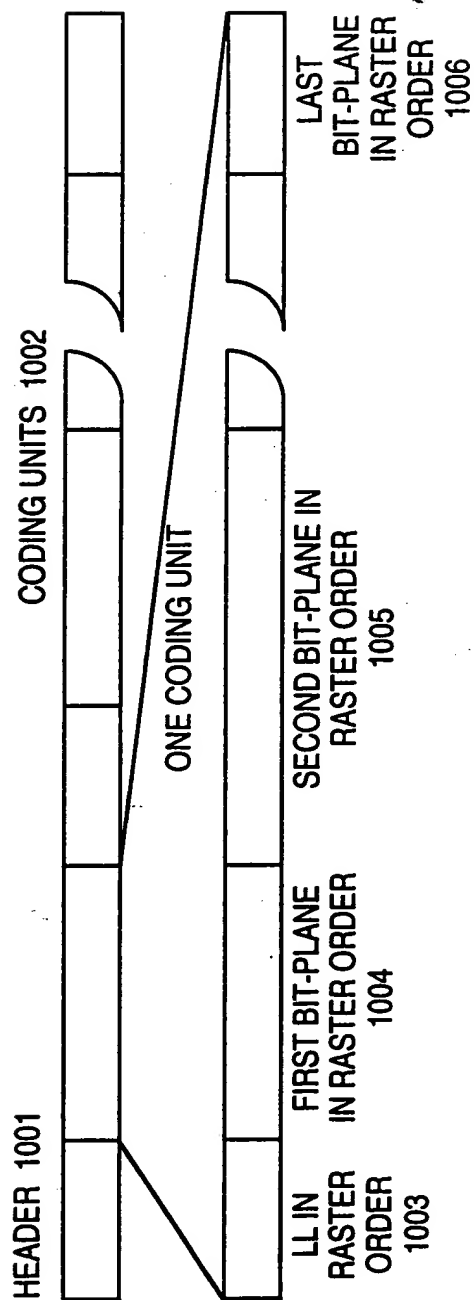


FIG. 9

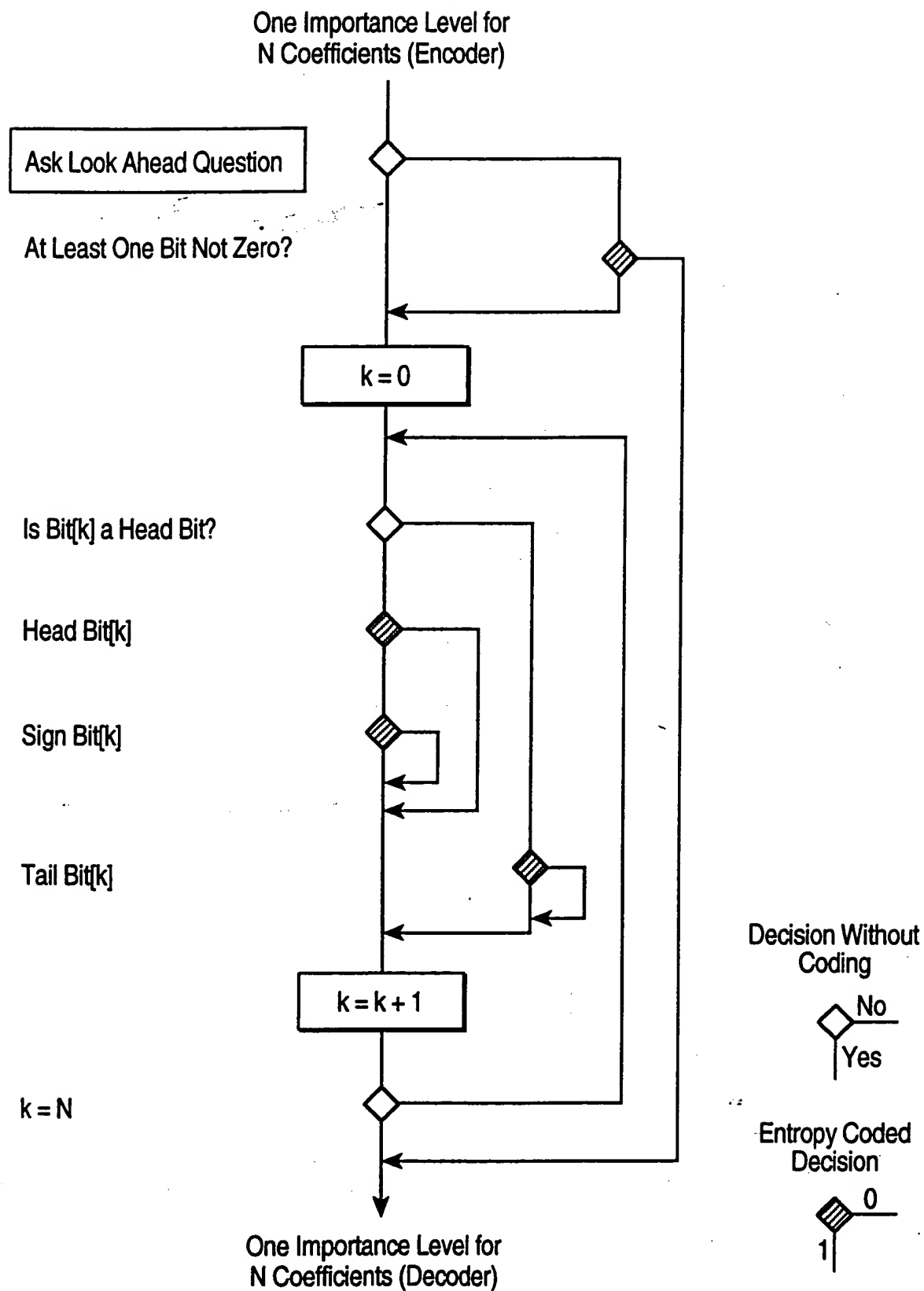


FIG. 11

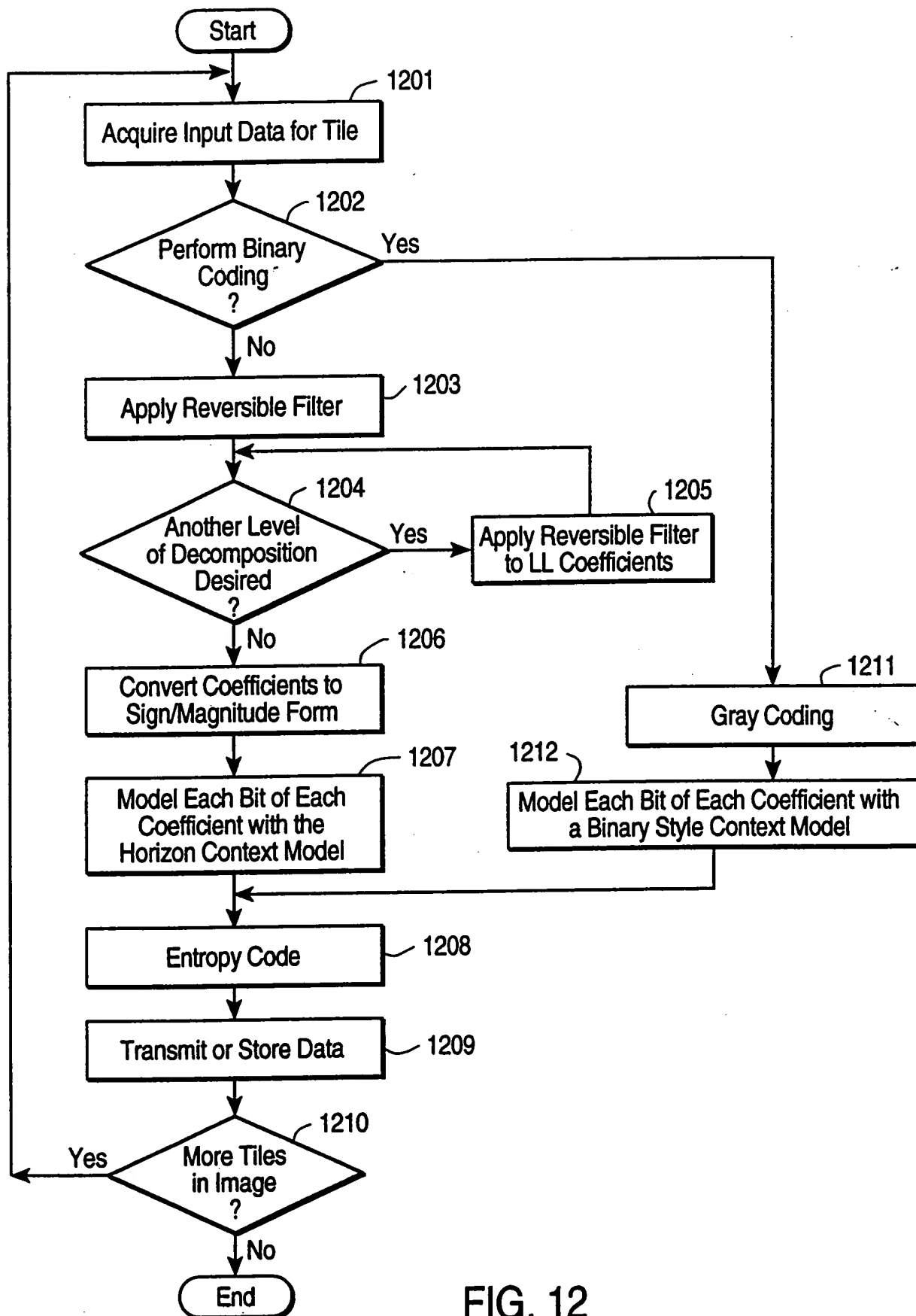


FIG. 12

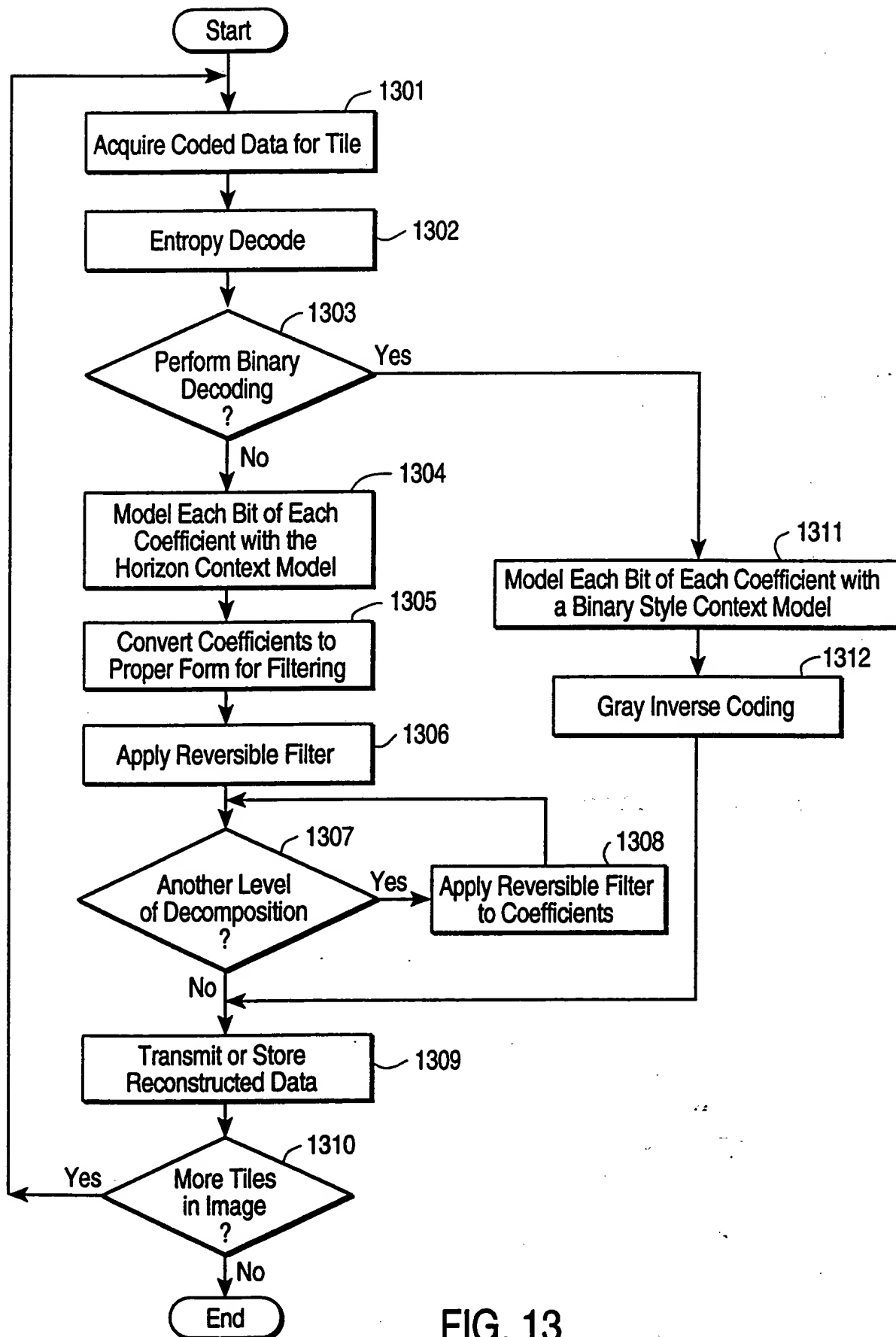


FIG. 13

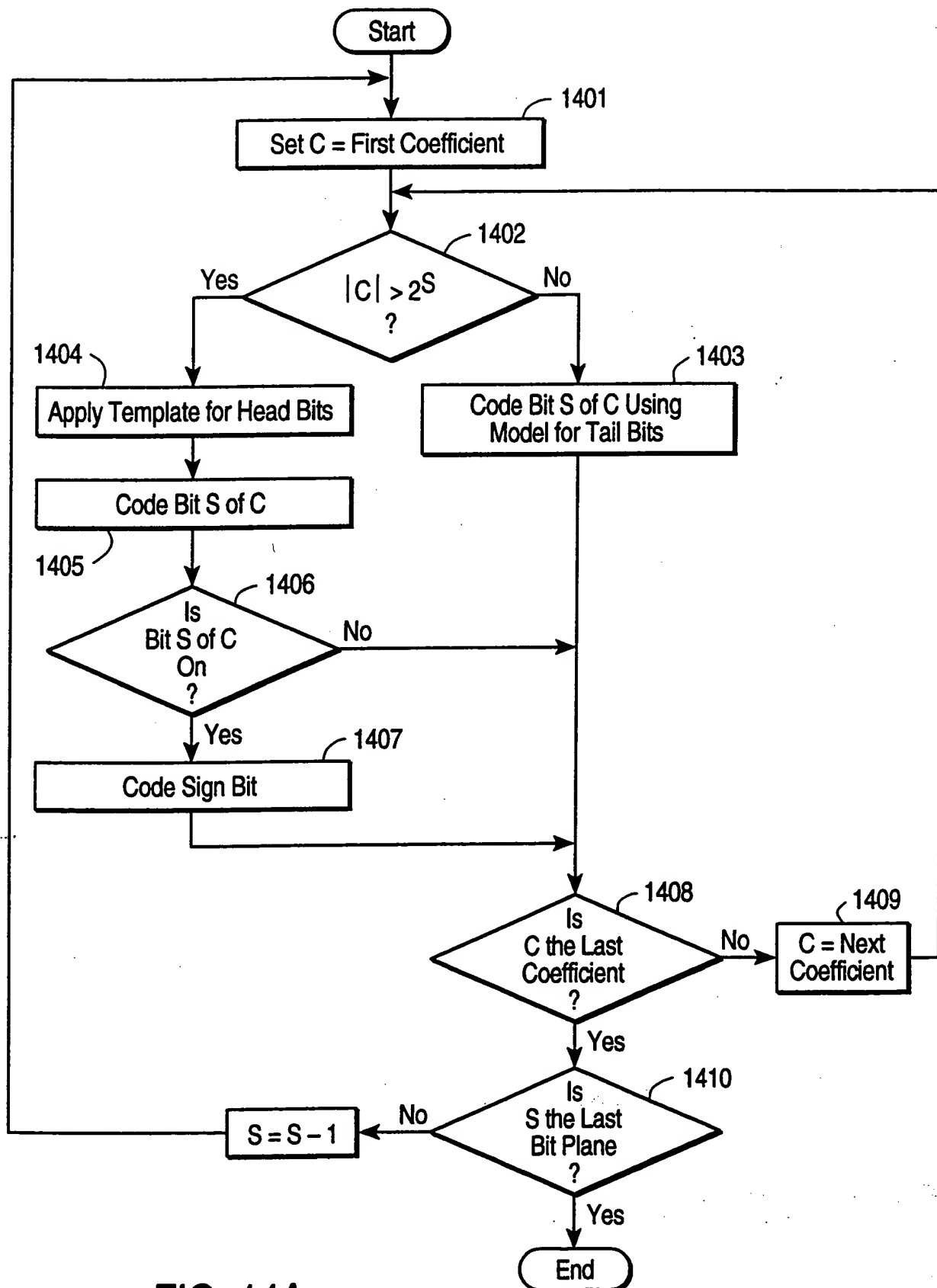


FIG. 14A

Parent (optional)

Tail Info (2)

	Tail On (1)	
Tail Info (2)		Tail On (1)
Tail On (1)	Tail On (1)	

FIG. 14B

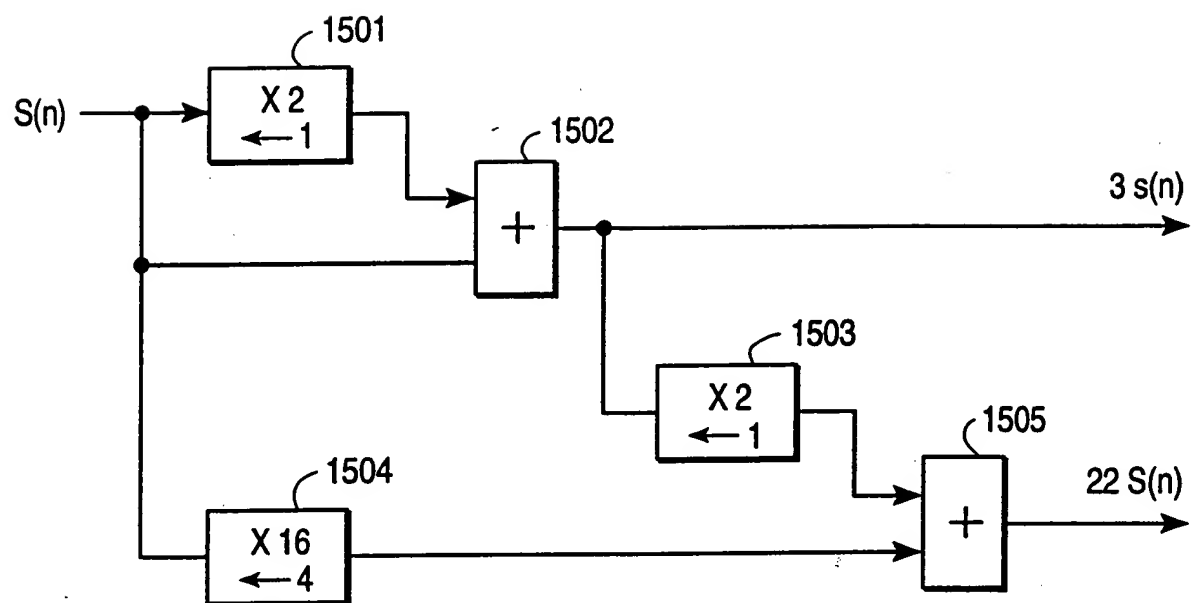


FIG. 15

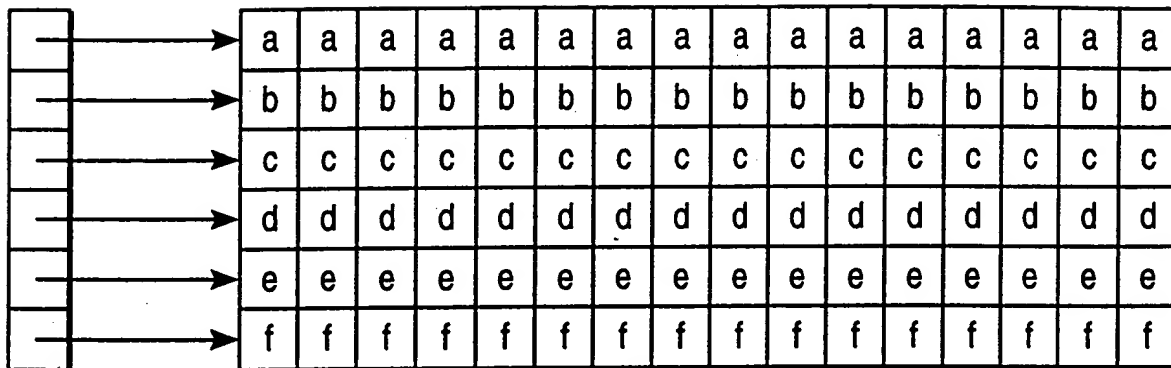


FIG. 16A

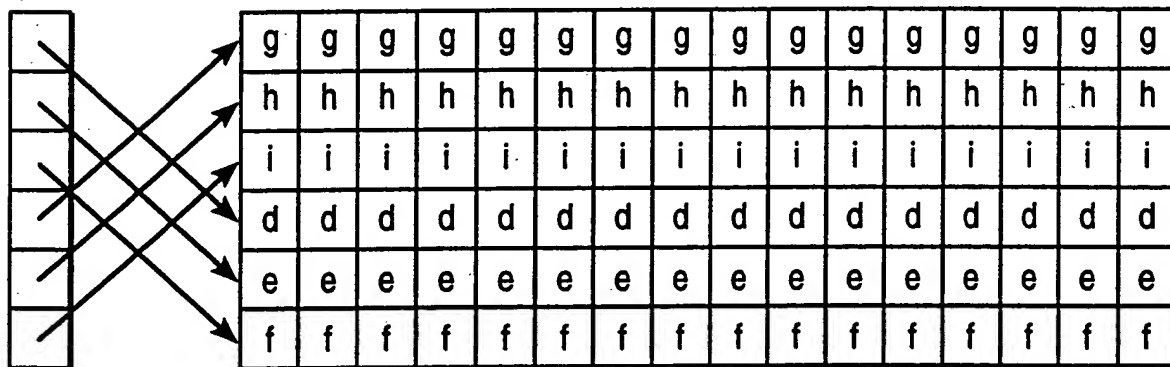


FIG. 16B

S_{n-1}	D_{n-1}	S_n	B_n	X_{2n+2}	X_{2n+3}	X_{2n+4}	X_{2n+5}
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FIG. 17A

S_{n-1}	D_{n-1}	S_n	B_n	S_{n+1}	B_{n+1}	X_{2n+4}	X_{2n+5}
-----------	-----------	-------	-------	-----------	-----------	------------	------------

FIG. 17B

S_{n-1}	D_{n-1}	S_n	D_n	S_{n+1}	B_{n+1}	X_{2n+4}	X_{2n+5}
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FIG. 17C

[illegible]

FIG. 18

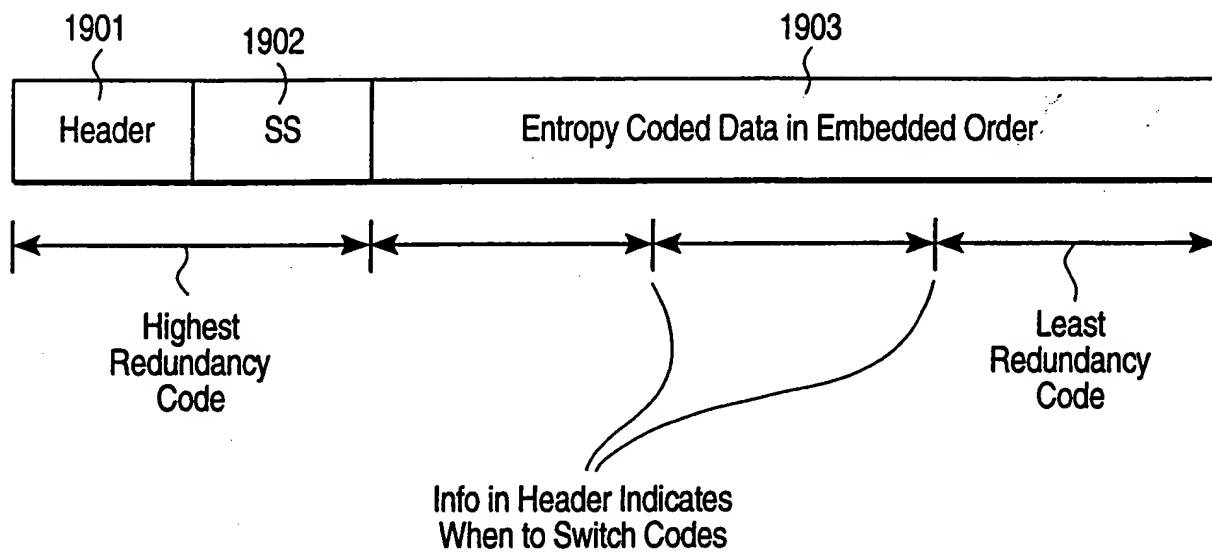


FIG. 19

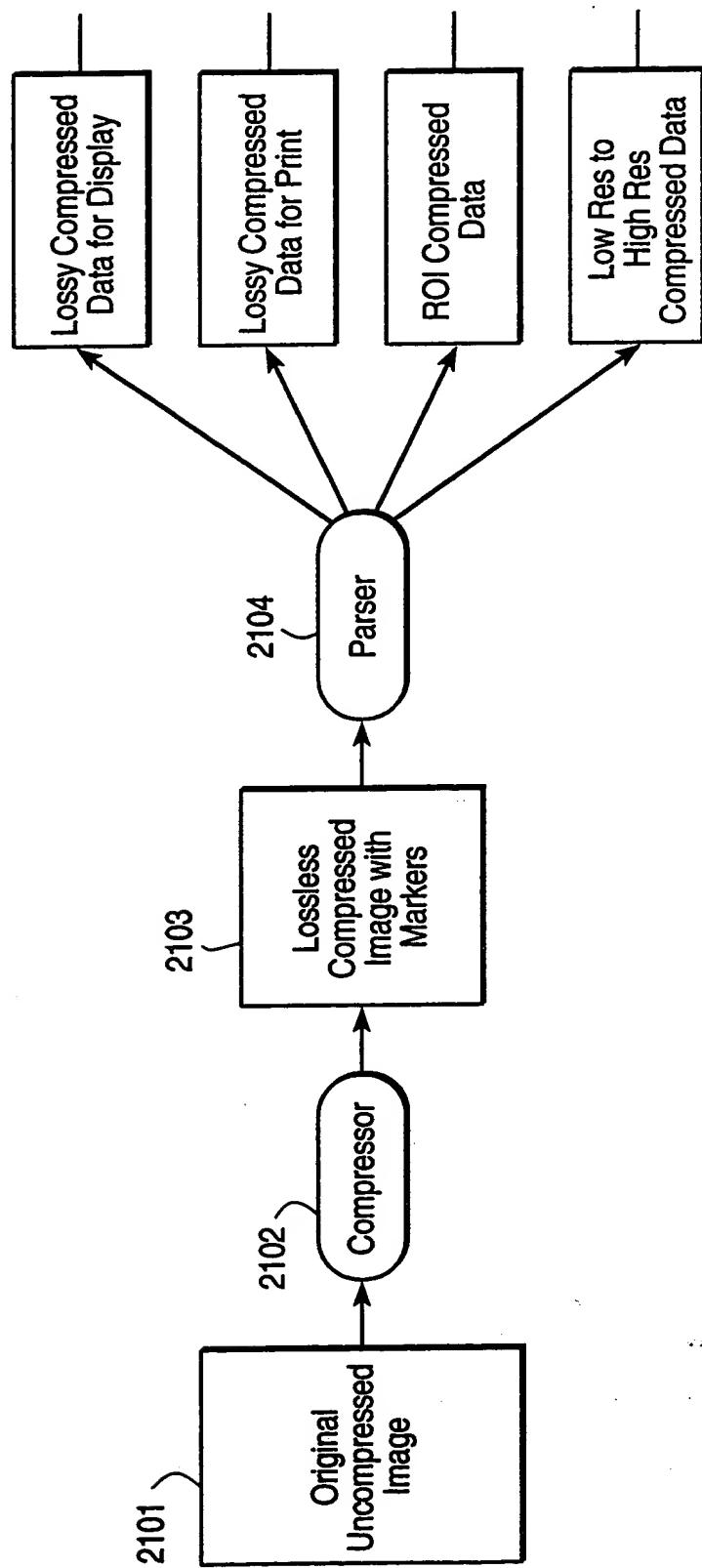


FIG. 21A

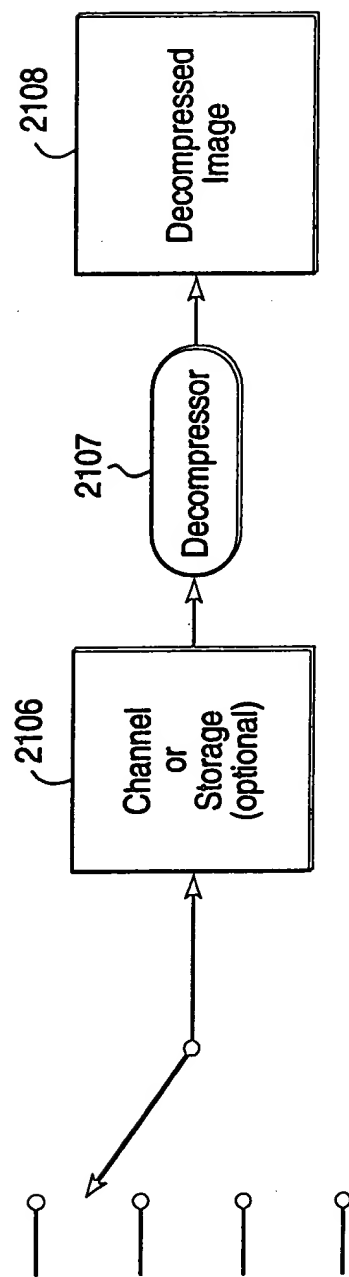


FIG. 21B

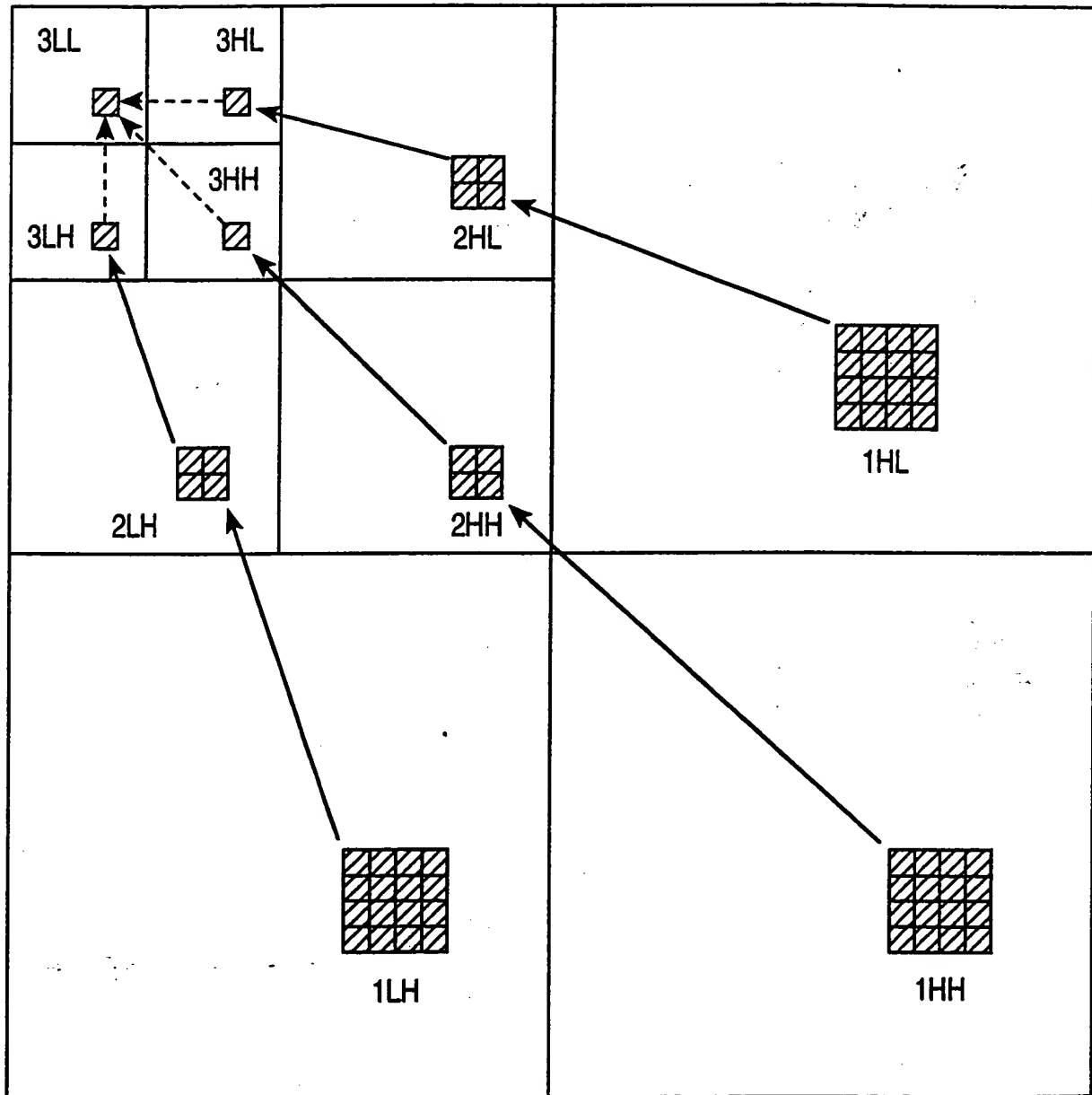


FIG. 22

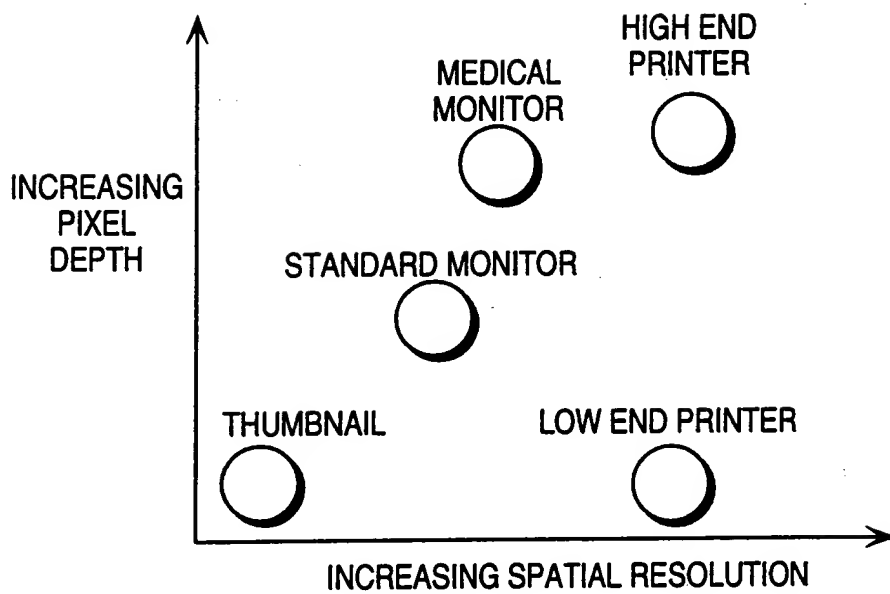


FIG. 23

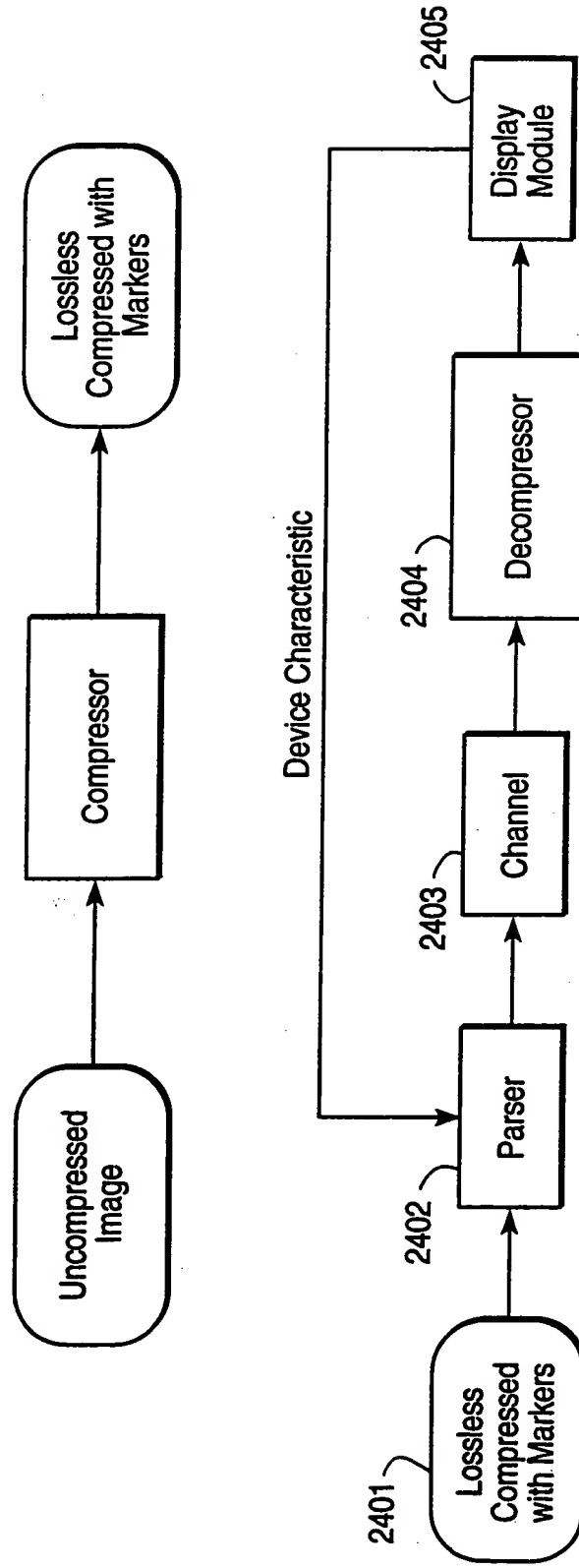


FIG. 24

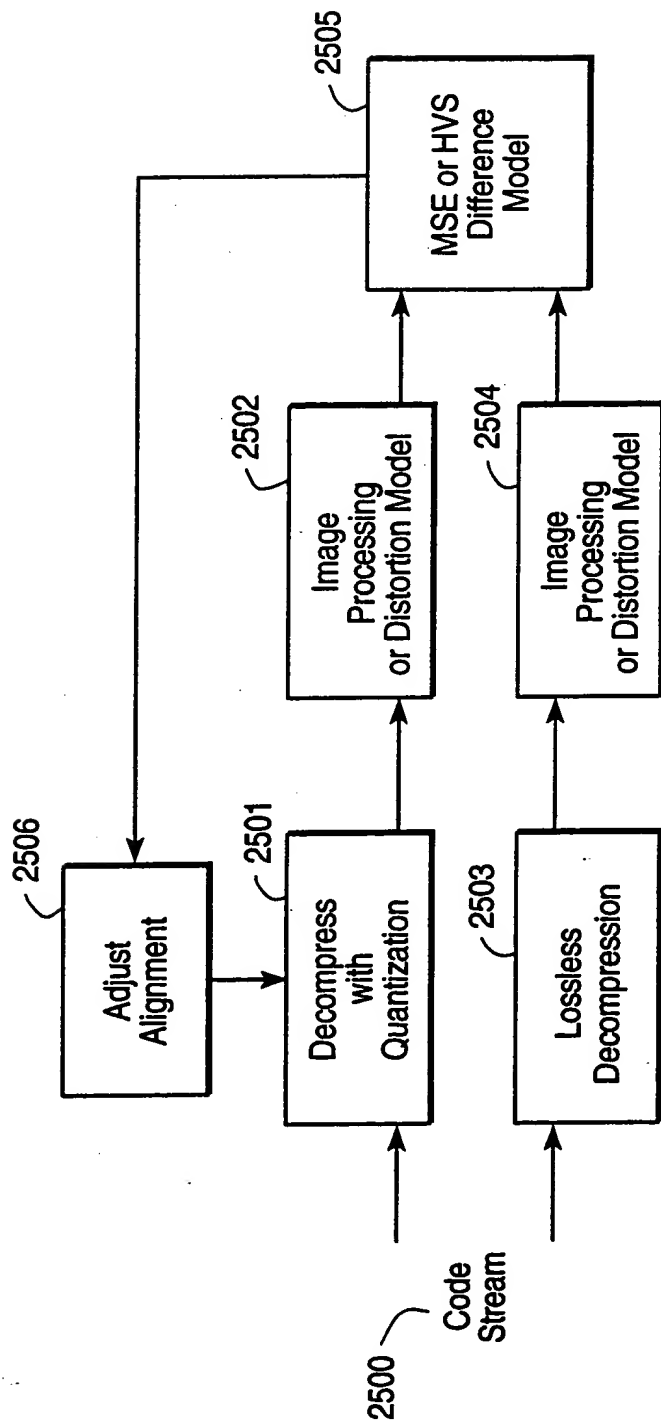


FIG. 25



FIG. 26A

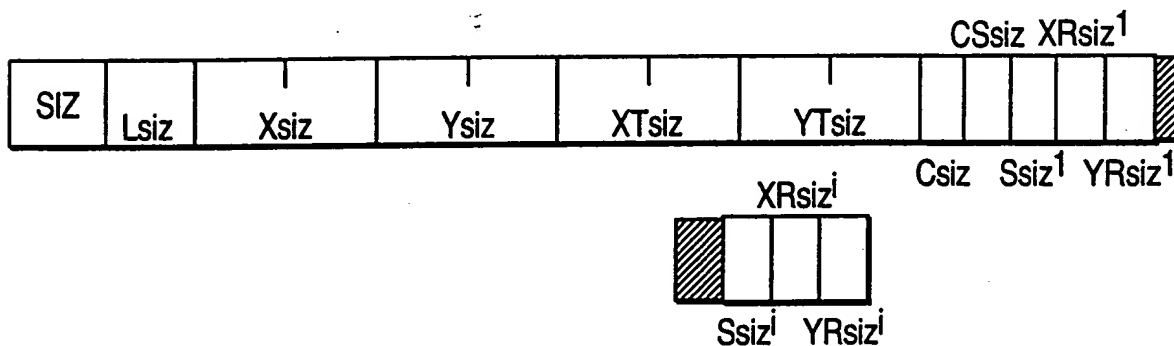


FIG. 26B

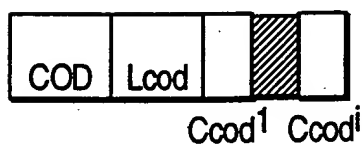


FIG. 26C

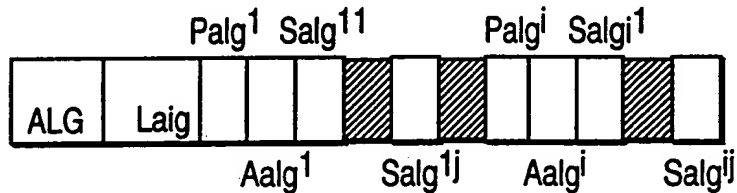


FIG. 26D

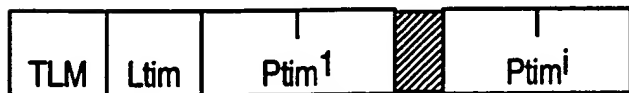


FIG. 26E



FIG. 26F

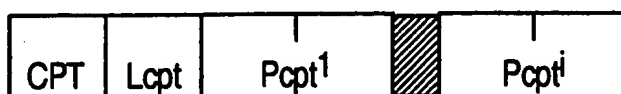


FIG. 26G

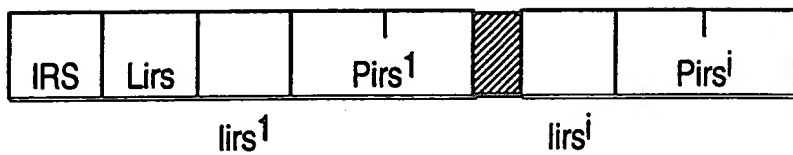


FIG. 26H

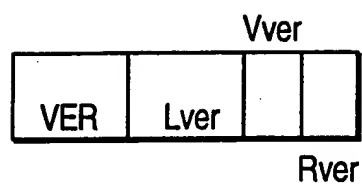


FIG. 26I

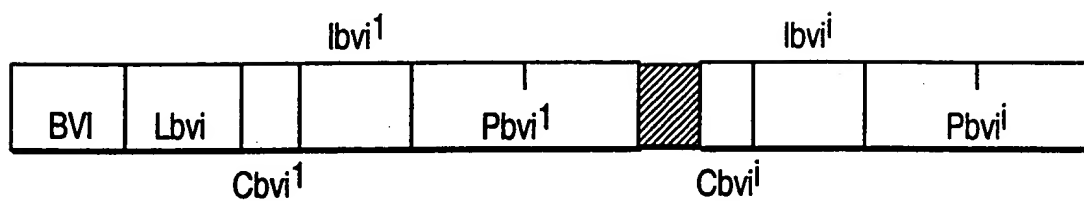


FIG. 26J

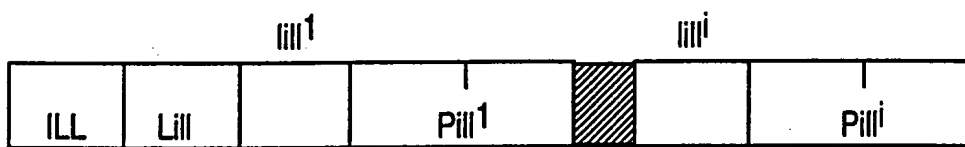


FIG. 26K

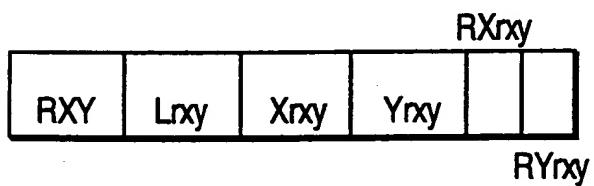


FIG. 26L

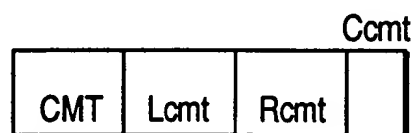


FIG. 26M

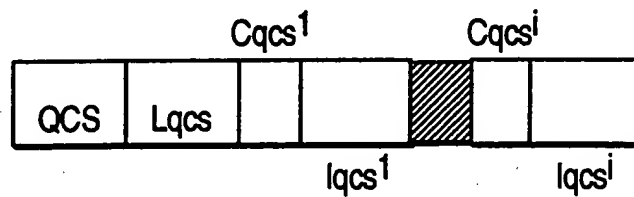


FIG. 26N

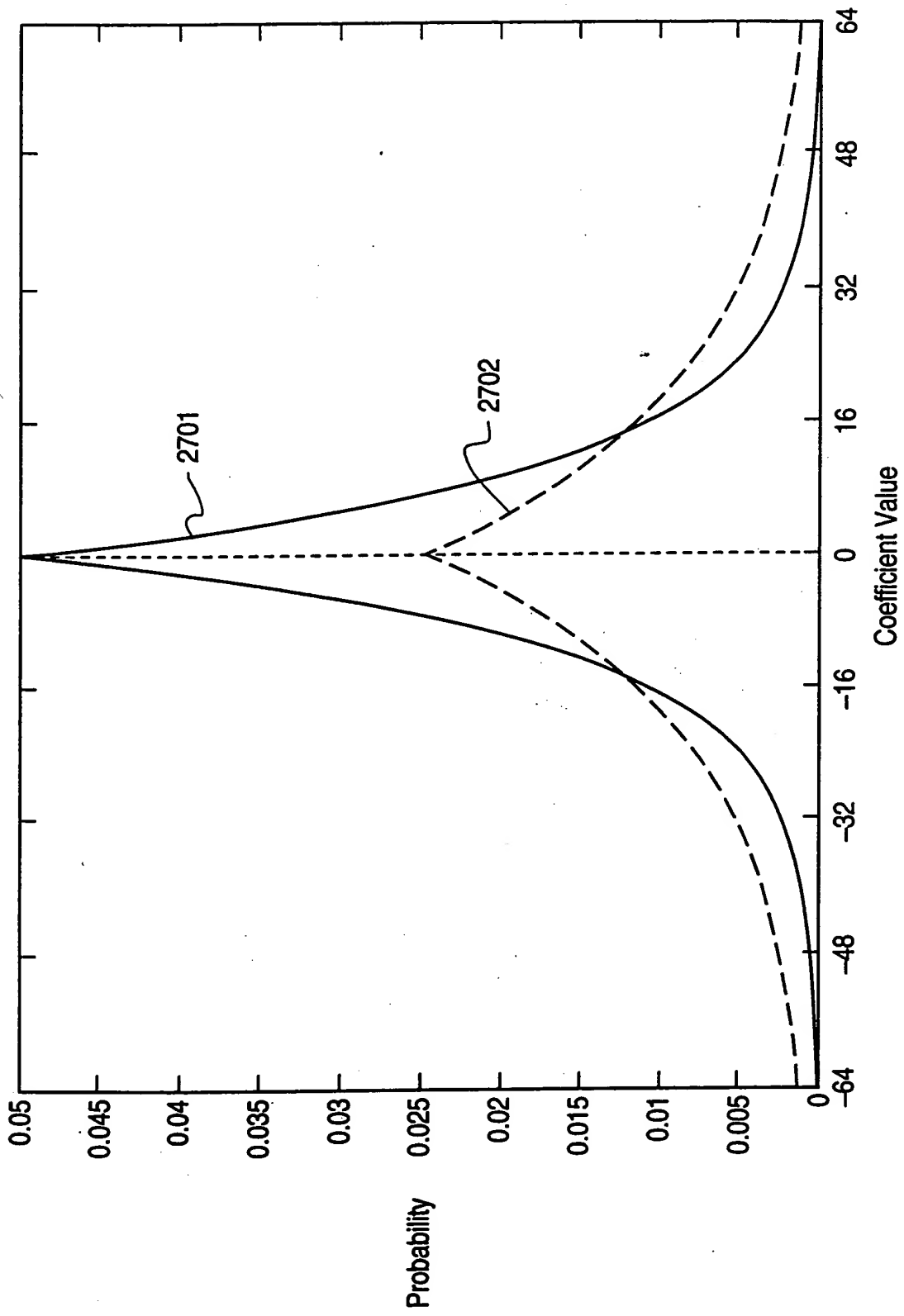
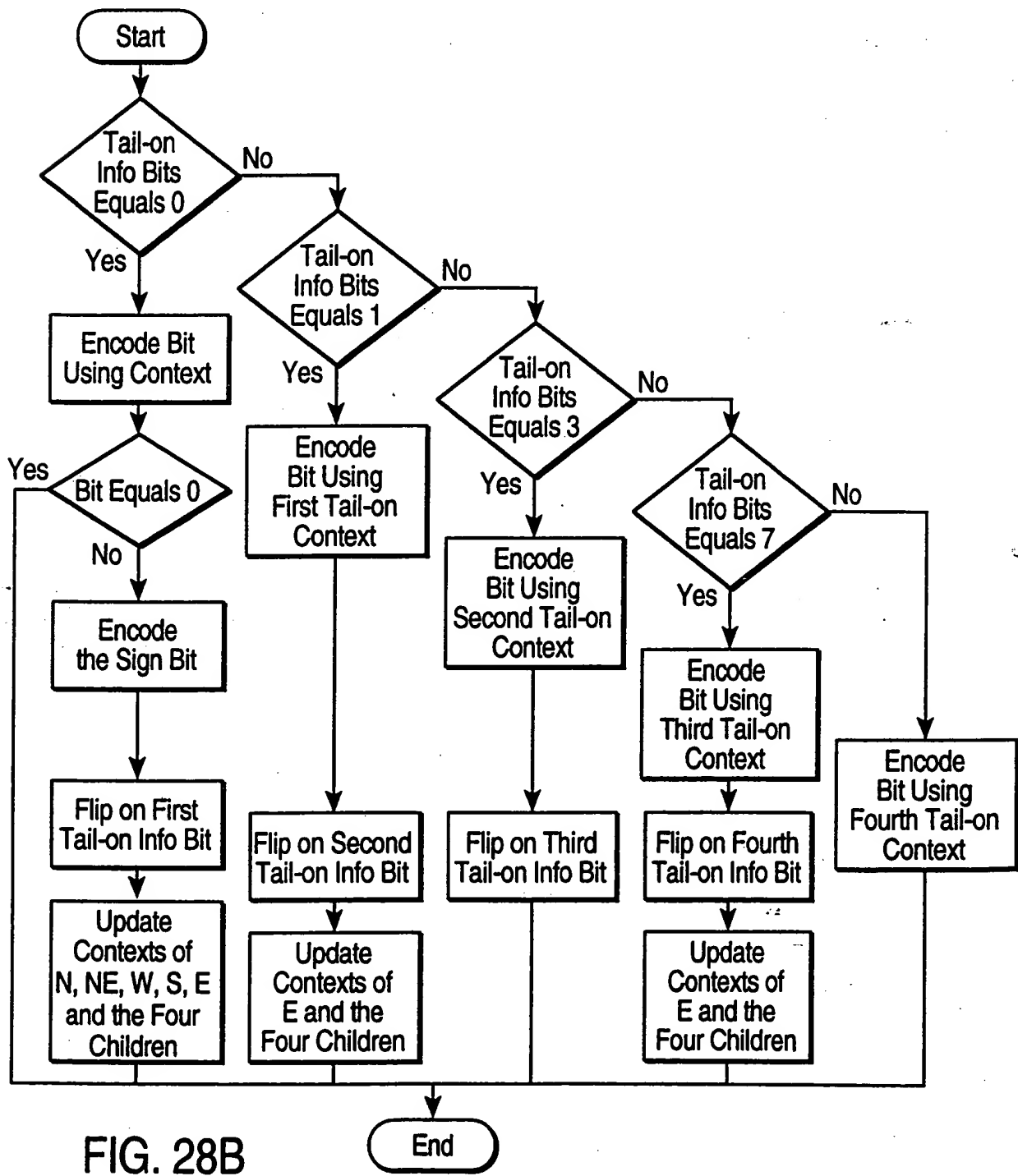
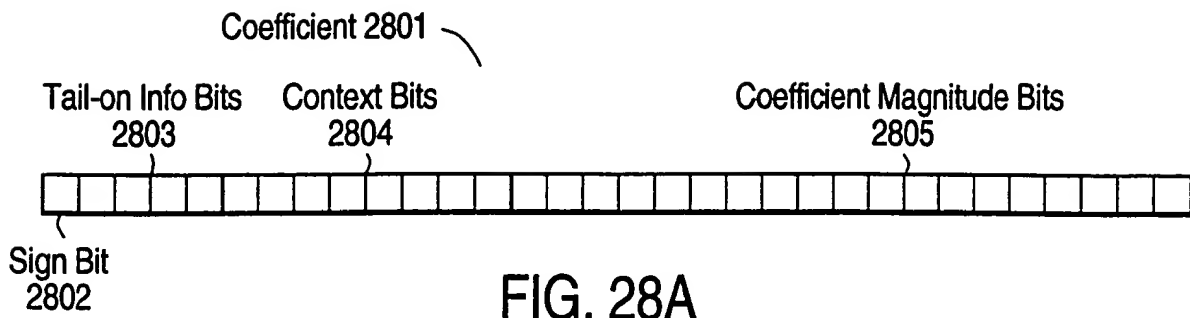


FIG. 27



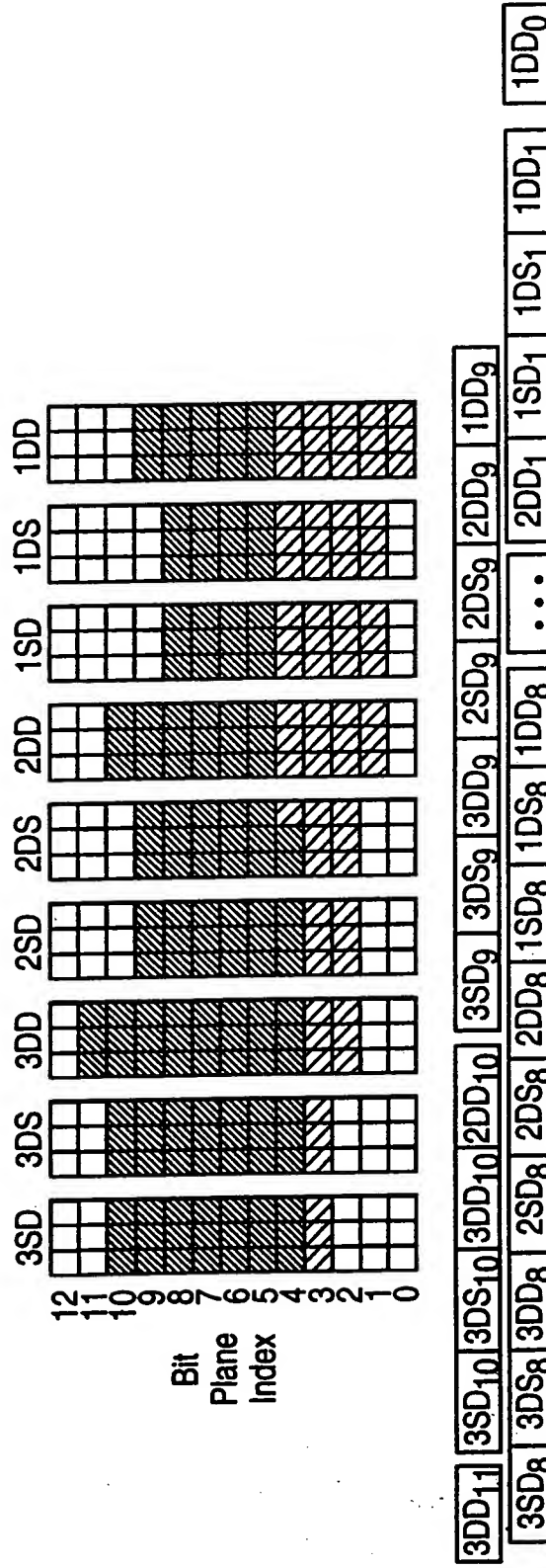


FIG. 29A

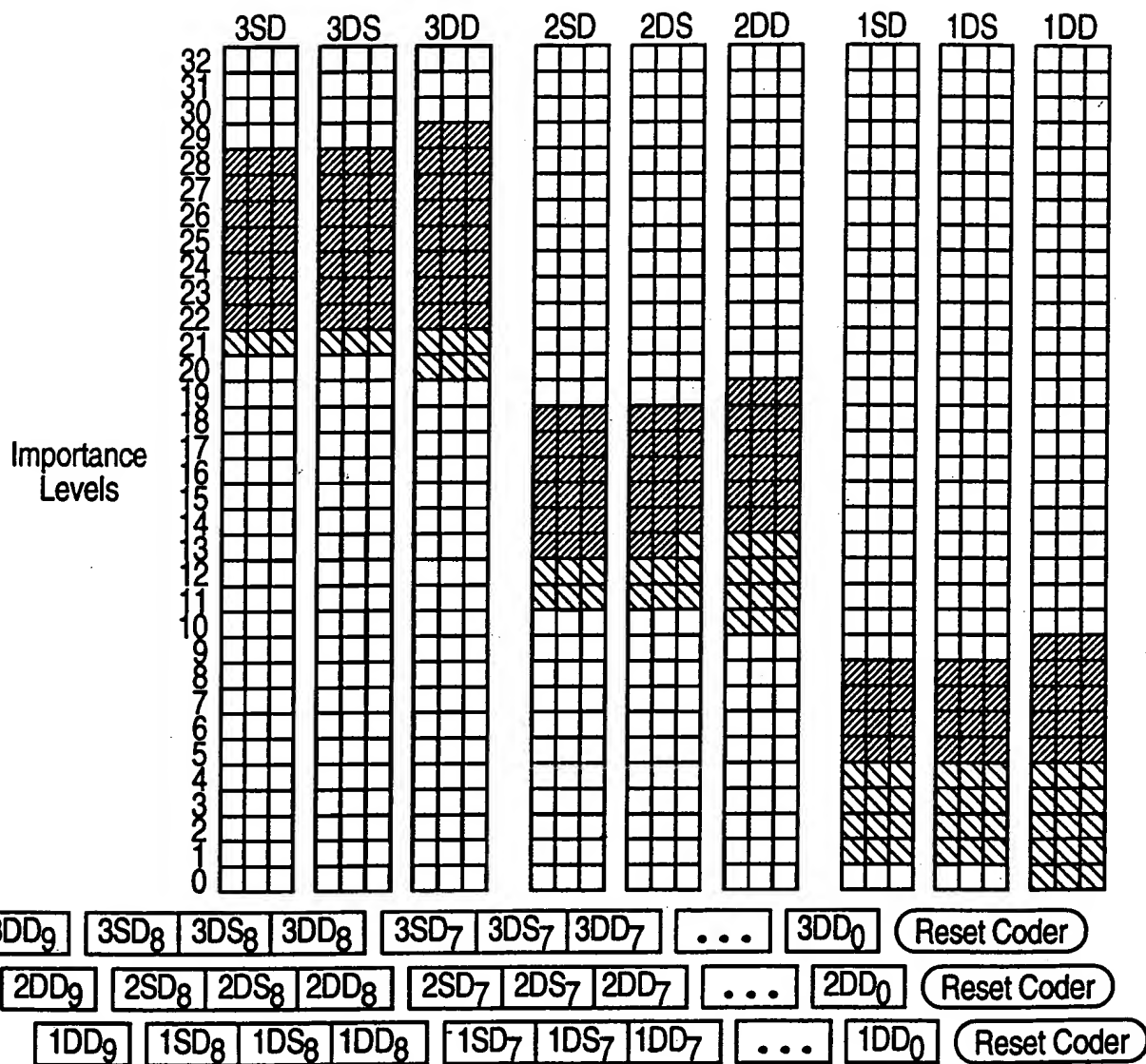


FIG. 29B

4HL	4LH	4HH	3HL	3LH	3HH	2HL	2LH	2HH	1HL	1LH	1HH

Fig. 29C

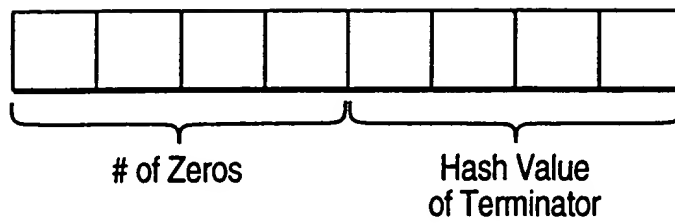


FIG. 30

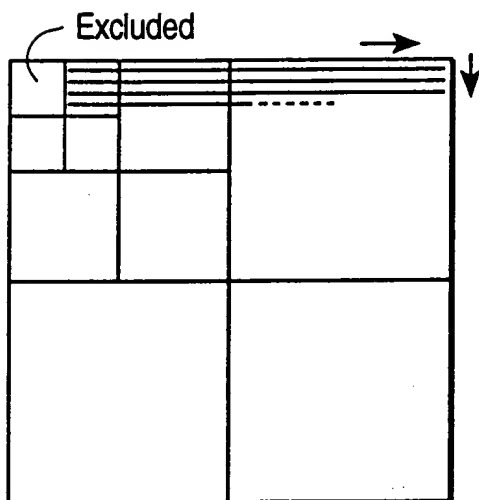


FIG. 31A

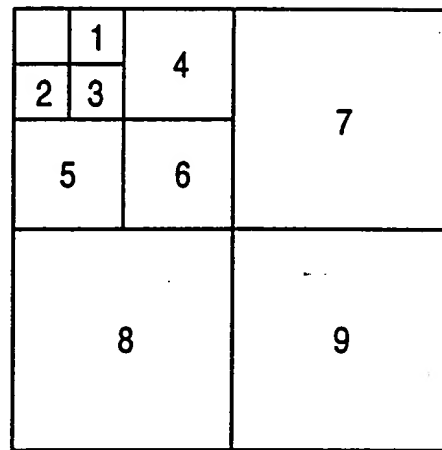


FIG. 31B

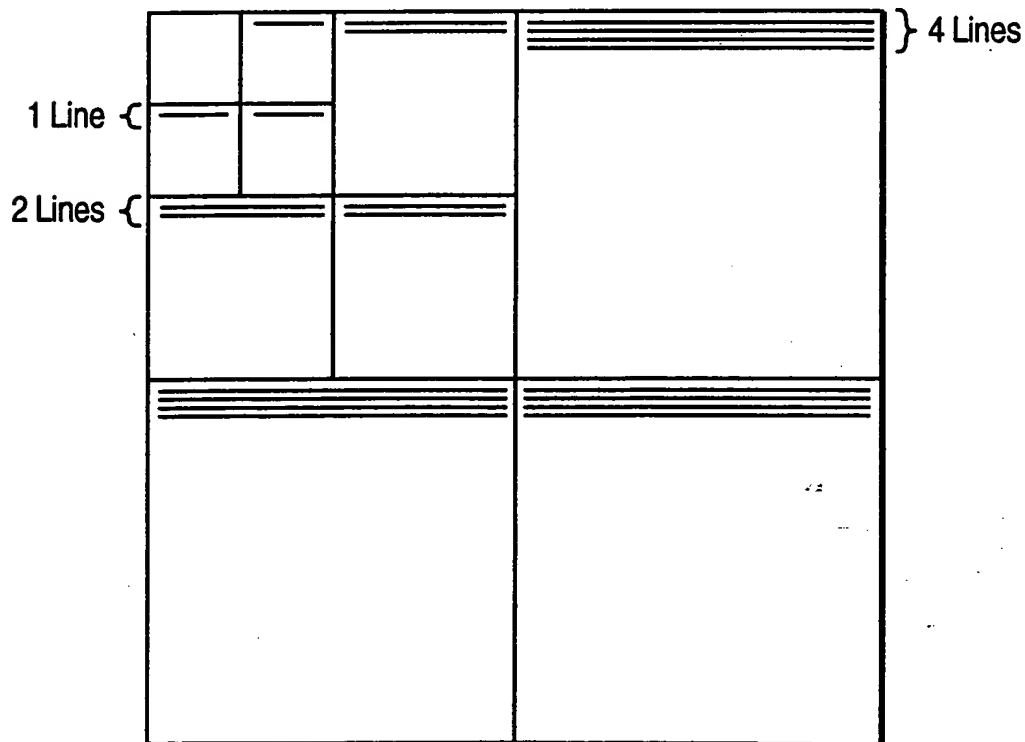


FIG. 31C

1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS	1SS	1DS
1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db

FIG. 32A

2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
2SD	1DS	2DD	1DS	2SD	1DS	2DD	1DS	2SD	1DS	2DD	1DS	2SD	1DS	2DD	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS	2SS	1DS	2DS	1DS
1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD	1SD	1DD
2Sb	1DS	2Db	1DS	2Sb	1DS	2Db	1DS	2Sb	1DS	2Db	1DS	2Sb	1DS	2Db	1DS
1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db	1Sb	1Db

FIG. 32B